

ABSTRACT OF THE DISCLOSURE

A color display device comprises a plurality of pixel regions having different areas, which may be realized by differences in the horizontal width, corresponding to different color components. Each pixel region is provided with a first thin film transistor for selectively supplying a signal corresponding to a display information to the pixel region, and a storage capacitor connected to the first thin film transistor for retaining the signal corresponding to the display information. A plan view distance obtained by projecting onto a display plane surface an extent from the capacitor-side end portion of a gate of the first thin film transistor to the storage capacitor is configured identical in the respective pixel regions. With this arrangement, the need to adjust for each color component the writing time and rate of the signal supplied via the first thin film transistor can be eliminated, even when the pixel regions have different areas.